



SRINIVASA INSTITUTE OF TECHNOLOGY & SCIENCE
(AUTONOMOUS)

Chennai-Hyderabad Bypass Road, Ukkayapalli, Kadapa, A.P-516002



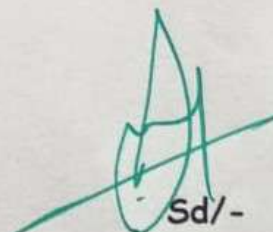
Examination Branch

M.Tech I Semester (SITS25) Regular Examinations-January/February-2026

Time: 10:00 AM to 01:00 PM

Date/Day	ELECTRICAL POWER SYSTEMS	VLSI SYSTEM DESIGN	COMPUTER SCIENCE & ENGINEERING
31.01.2026 Saturday	Advanced Power System Protection 25MEP101	CMOS Analog IC Design 25MVD101	Advanced Data Structures & Algorithms 25MCS101
03.02.2026 Tuesday	Power System Security and State Estimation 25MEP102	CMOS Digital IC Design 25MVD102	Distributed Operating Systems 25MCS102
05.02.2026 Thursday	Energy Auditing and Management 25MEP103a	Microchip Fabrication Techniques 25MVD103a	Advanced Computer Architecture 25MCS103a
07.02.2026 Saturday	Smart Grid Technologies 25MEP104b	FPGA Architectures and Applications 25MVD104b	Natural Language Processing 25MCS104b
09.02.2026 Monday	Research Methodology and IPR 25MRM101		

Notification Date: 08.01.2026



Sd/-
Controller of Examinations